



Attorney Docket No. 1448.1047

Co/c

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Tetsuya YOSHIDA, et al.

Application No.: 10/715,366

ATTENTION

Confirmation No.: 3769

CERTIFICATE OF CORRECTION

Filed: November 19, 2003

BRANCH

U.S. Patent No.: 7,174,435

Issued: February 6, 2007

For: MEMORY CONTROL CIRCUIT, MEMORY DEVICE, AND MICROCOMPUTER

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

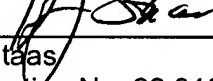
Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: April 13, 2007

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Certificate
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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 7,174,435

DATED : February 6, 2007

INVENTOR(S) : Tetsuya YOSHIDA, et al.

Column 6, Line 32, change "pate" to --gate--.

Column 6, Line 34, change "pate" to --gate--.

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PATENT NO. 7,174,435

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